

FIG. 3
OVERALL LOGIC FOR
DETERMINING "LAMB" SET OF NODES

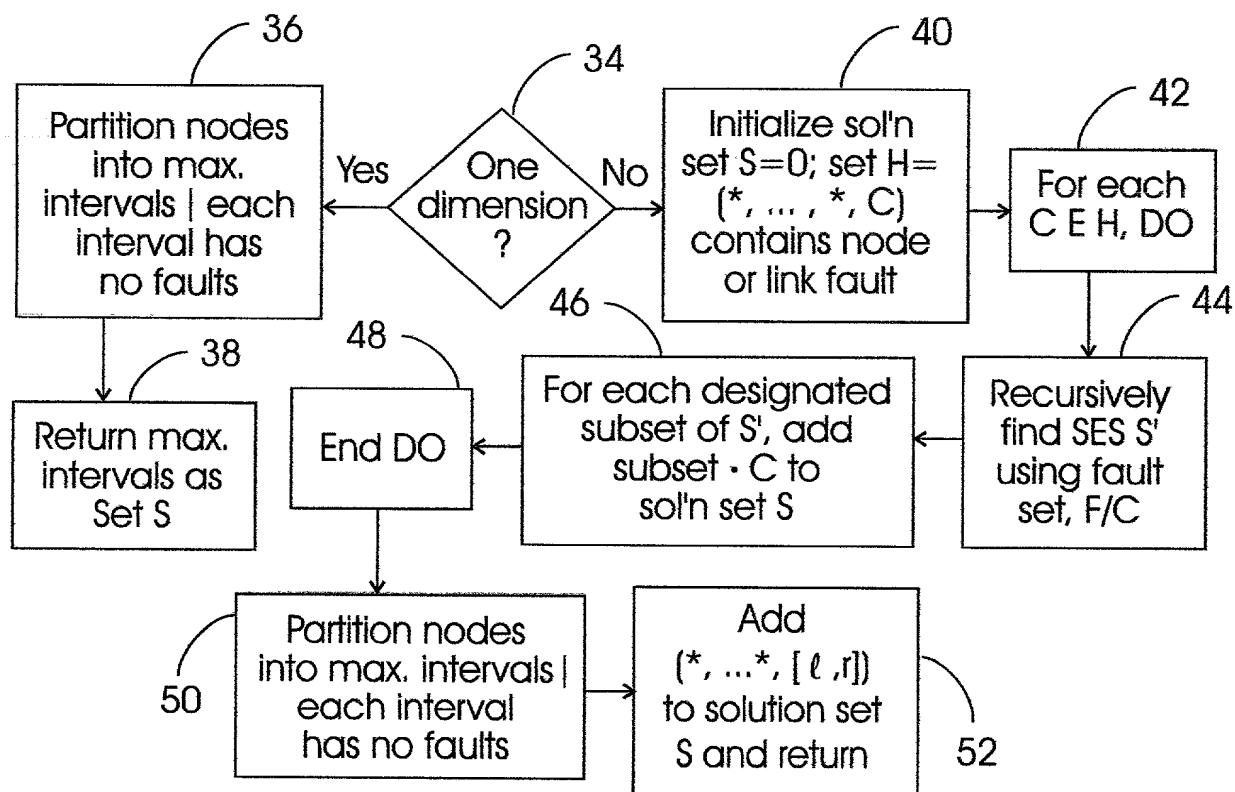


FIG. 4
LOGIC FOR FINDING PARTITIONS

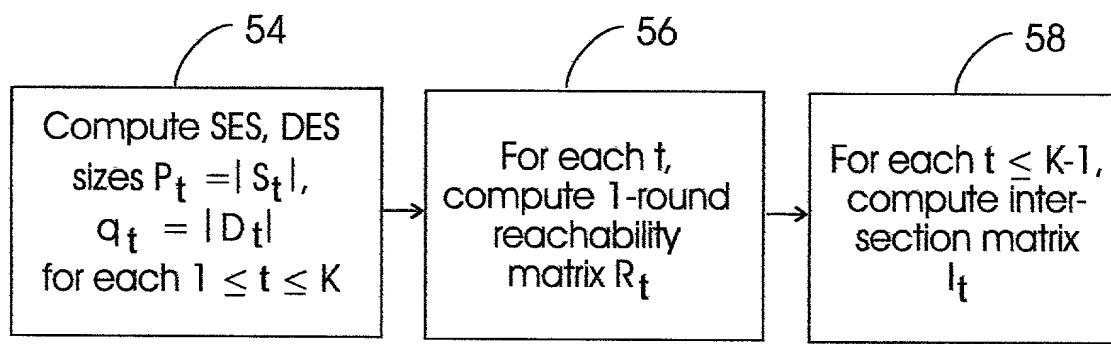


FIG. 5
LOGIC FOR
FINDING
REACHABILITY

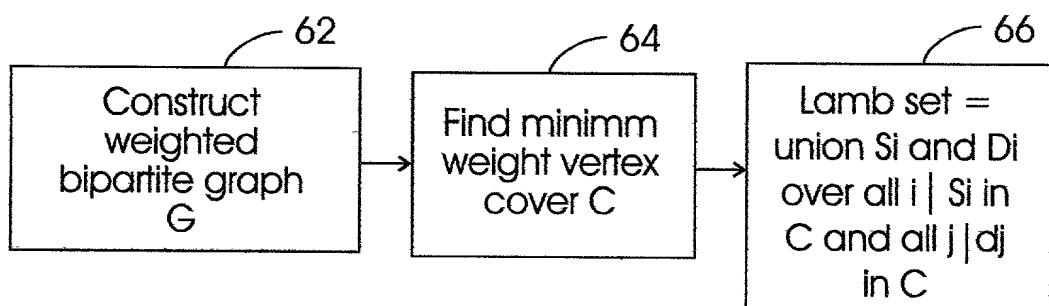


FIG. 6
PREFERRED LOGIC FOR
REDUCTION TO WVC

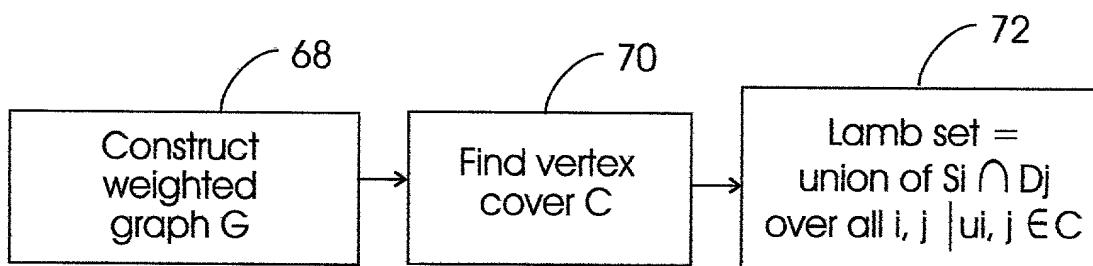


FIG. 7
ALTERNATE LOGIC FOR
REDUCTION TO WVC